



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in light of NEP-2020**  
**Diploma in Electronics**

**SEMESTER- V (2023-2026)**

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assess-ment* (20%)	End Sem University Exam (60%)	Teachers Assess-ment* (40%)	
1	DTEE601N	Control Systems	3	1	2	5	60	20	20	30	20	150
2	DTET502	Embedded Systems	3	0	2	4	60	20	20	30	20	150
3	DTEE502	Industrial Electronics	3	0	2	4	60	20	20	30	20	150
4	DTET504	PLC Lab	0	0	4	2	0	0	0	30	20	50
5	-	Generic Elective III	4	0	0	4	60	20	20	0	0	100
6	DTEEE501	Minor Project	0	0	4	2	0	0	0	90	60	150
7	DTEEE502	Industrial Training	0	0	2	1	0	0	0	30	20	50
TOTAL			13	1	16	22	240	80	80	240	160	800

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

**Chairperson**  
 Board of Studies  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore

**Chairperson**  
 Faculty of Studies  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore

**Controller of Examinations**  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore

**Registrar**  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore

**Vice Chancellor**  
 Shri Vaishnav Vidyapeeth  
 Vishwavidyalaya, Indore